

10/630,957

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	logic model\$1 same (physical (BIT-HW) or built-in test hardware)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:10
L2	5056	test vector\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:10
L3	2	I1 and I2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:10
L4	1082	compress\$3 and (test vector\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:11
L5	2	I3 and I4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:11
L6	4391	function operator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:11
L7	54	initial state function	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:11
L8	48	IC design data	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:11

## EAST Search History

L9	2	executable logic model	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:11
L10	895	logic model\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:12
L11	7	l10 and (l6 or l7 or l8 or l9)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:26
L12	3	l11 and (l1 or l2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:26
L13	2	l12 and l4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:21
L14	16321	714/738 or 714/733 or 714/734 or 714/30 or 714/726 or 716/4 or 716/1 or 716/? or 365/201	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:18
L15	1	l14 and l12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:18
L16	19769	703/? or l14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:26

## EAST Search History

L17	1	I11 and I16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/07/20 17:26
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